

CONSTRUCTION OF ELECTRONIC PARTS

Publication number: JP60049638 (A)

Publication date: 1985-03-18

Inventor(s): OZAWA KAZUHITO

Applicant(s): SHARP KK

Classification:

- **international:** **H01L21/60**; H05K3/06; H05K3/34; H05K3/40; **H01L21/02**; H05K3/06; H05K3/34; H05K3/40; (IPC1-7): H01L21/60

- **European:** H01L21/60

Application number: JP19830156747 19830826

Priority number(s): JP19830156747 19830826

Abstract of JP 60049638 (A)

PURPOSE:To attain rapidly positioning of an electronic parts by a method wherein spherical bumps are formed to the bonding terminals of an LSI, and pin holes are formed to a corresponding wiring substrate. **CONSTITUTION:**Recess shapes 15 are formed previously at the bonding positions of wiring terminals 13. To form the shapes thereof, after the terminals 13 are formed, an etching mask is covered thereon, and windows are opened in a pin hole type in the mask only at the parts corresponding to the bonding positions. Etching is performed, and semispherical recess parts 15 are formed only at the parts opened with the windows according to half etching. The bumps 12 of an LSI formed in a semispherical type are positioned to be bonded to the terminals 13 formed in such a way.

